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APPLICANTS

Hideo Akiyoshi, Kawasaki, JAPAN;

** CONTINUING DATA ***** *YES* *****
This application is a CON of 09/610,982 07/06/2000 ABN

** FOREIGN APPLICATIONS ***** *NO* *****
JAPAN 11-192375 07/06/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 01/12/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 6
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35 USC 119 (a-d) conditions met ☒ yes ☐ no ☐ Met after Allowance

Verified and Acknowledged *Phan* Examiner's Signature *TP* Initials

ADDRESS
21171
STAAS & HALSEY LLP
SUITE 700
1201 NEW YORK AVENUE, N.W.
WASHINGTON, DC
20005

TITLE
Latch circuit having reduced input/output load memory and semiconductor chip

FILING FEE RECEIVED 992	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input checked="" type="checkbox"/> All Fees <input checked="" type="checkbox"/> 1.16 Fees (Filing) <input checked="" type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input checked="" type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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